

=	റ	RM	PTO.	.144	.9(M	odifie	d١

LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT

ATTY DONKET NO. A0521/7145	SERIAL NO. 09/054,864			
APPLICANT Craig R. Frink et al.				
FILING DATE April 3 1998	GROUP 2713			

## **U.S. PATENT DOCUMENTS**

Exam Init	Ref Des	Document No.	Date	Name	Class	Sub Class	FILING DATE If Appropriate
						C	$\pi$
						270	
	<u> </u>					1 2	8 1
						3	

FOREIGN PATENT DOCUMENTS

			OREIGN PATENT DOCUMENTS				
	Country & Doc. No. (11)	Pub. Date (43)		Class	Sub Class	了 Trans Yes	lation No
HVI	EP 0 713 308 A2	22.05.96	MATSUSHITA ELECTRIC INDUSTRIAL				
HVT	EP 0 833 514 A2	01.04.98	SONY CORPORATION				
				<u> </u>			
				<u> </u>			
				· ·			
1 1		1			i		

## OTHER ART

(Including Author, Title, Date, Pertinent Pages, Publication, Etc.)

Bursky, Dave, "CHIP SET SIMPLIFIES HIGH-SPEED INTERCONNECTS," Electronic Design, Vol. 42, No. 5, March 1994, pages 114-117
Hoffman, G. et al., "IEEE 1394: A UBIQUITOUS BUS," Digest of Papers of the Computer Society  Technologies for the Information SuperHighway, San Francisco, March 1995, pages 334-338
Hoffman. Dave. "STUDIO-INTERFACES FUR DIE PAKETIERE UBERTRAGUNG VON BITRATENREDUZIERTEN VIDEOSIGNALEN." Fernesh - Und Kino - Technik. Vol. 50.
No. 6, June 1996, pages 300-308

* a copy of this reference is not provided as it was previously	cited by or submitted to the office in a prior application, Serial No.
, filed	, and relied upon for an earlier filing date under 35 U.S.C. 120
(continuation, continuation-in-part, and divisional applications)	•

EXAMINER Later	DATE CONSIDERED 5/2/02
- Tuk	

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered.

Include copy of this form with next communication to applicant.